

PATENT NUMBER

O.I.P.E.	PATENT DATE
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APPLICATION NO. 09/920978	CONT/PRIOR D	CLASS 438	SUBCLASS 424	ART UNIT 2813	EXAMINER B/um
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Shane Trapp

BLUM

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Method of forming integrated circuitry and method of forming shallow trench isolation in a semiconductor substrate

PTO-2040
12/99[illegible]☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.		CLAIMS ALLOWED Total Claims Print Claim for O.G.	
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	_____ (Primary Examiner) (Date)		ISSUE BATCH NUMBER	
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